

INSTANTANEOUS
OUTPUT POWER
(dBm)

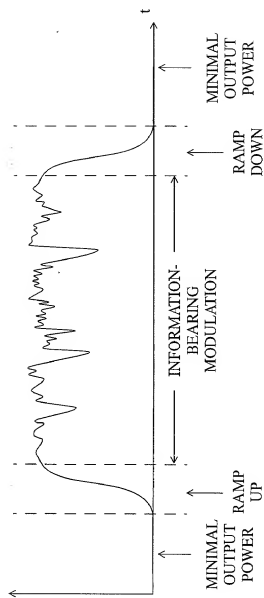


Fig. 1

(PRIOR ART)

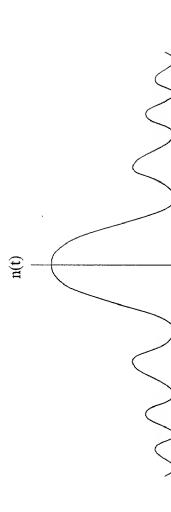


Fig. 13

(PRIOR ART)

Fig. 2

(PRIOR ART)



Fig. 3

(PRIOR ART)

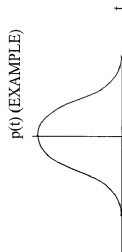


Fig. 4



Fig. 5

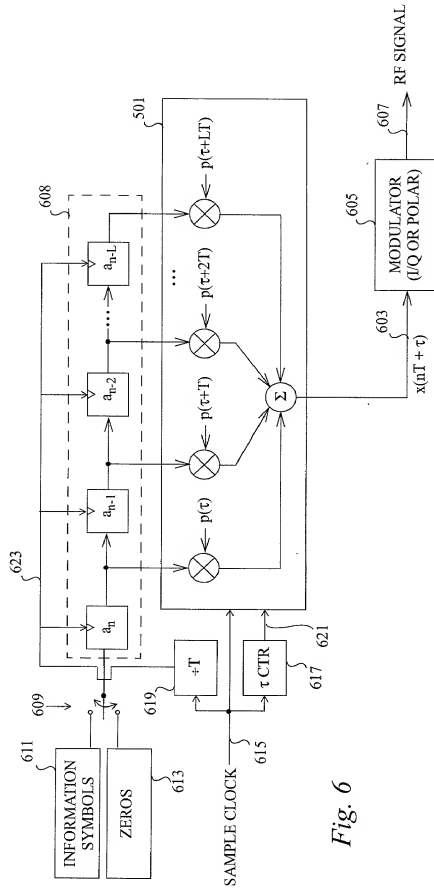
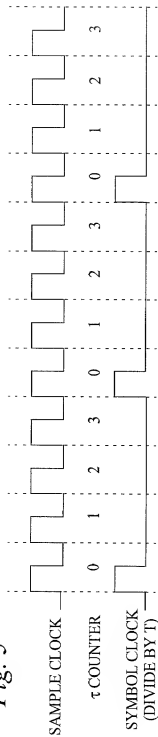


Fig. 6

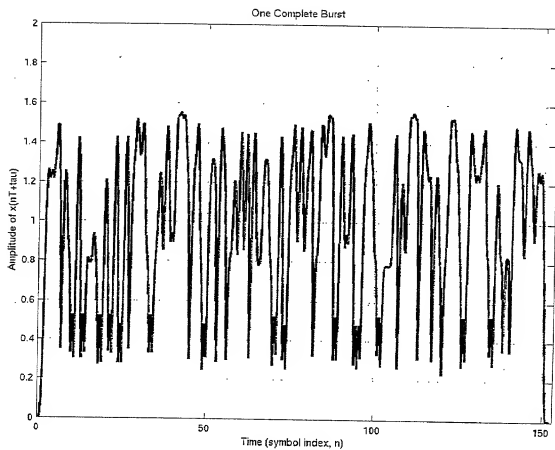


Fig. 7

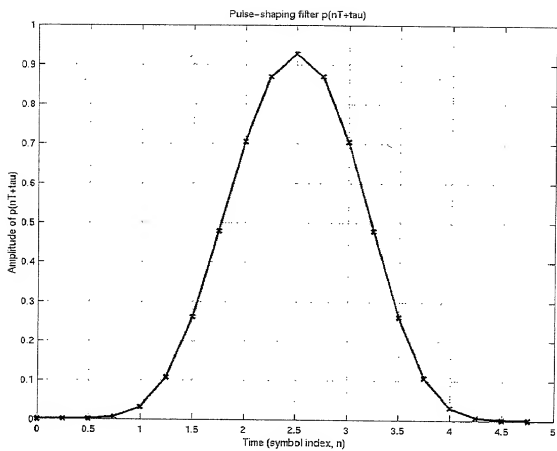


Fig. 8

0083967-04110

Shift register
contents
 a_n
 a_{n-1}
 a_{n-2}
 a_{n-3}
 a_{n-4}

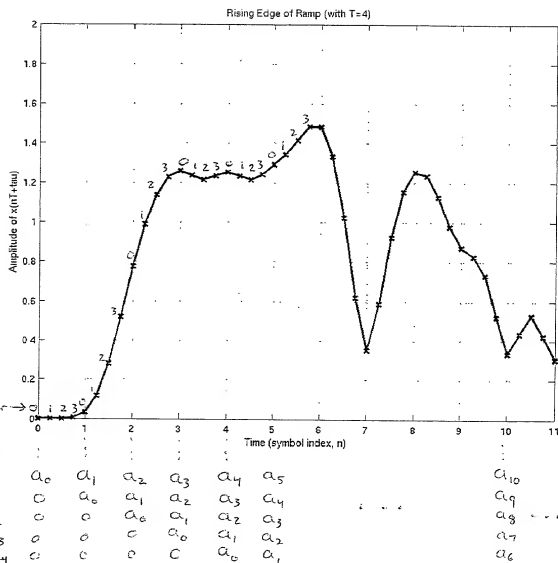


Fig. 9

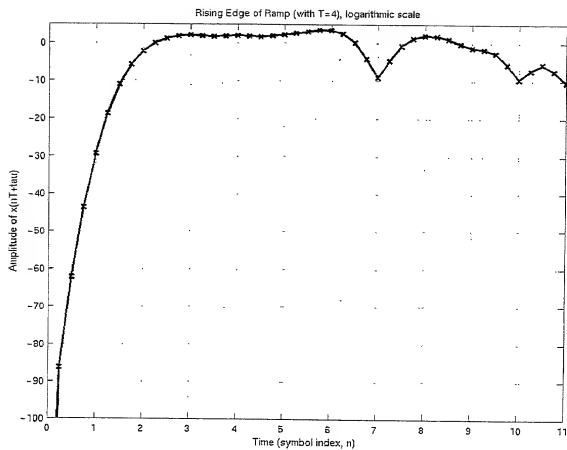
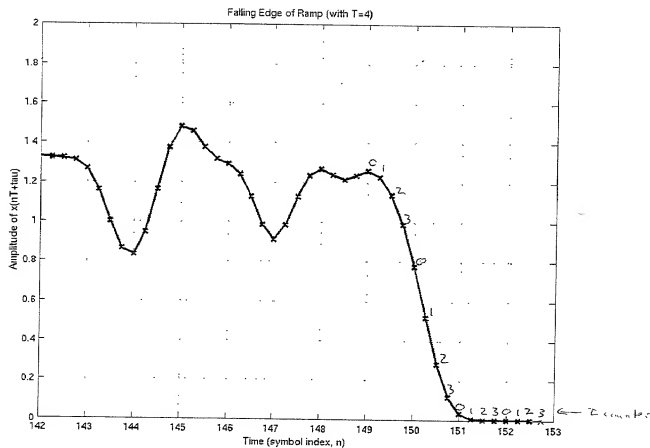


Fig. 10

00833967-04110



Shift
register
constants

a_n
 a_{n-1}
 a_{n-2}
 a_{n-3}
 a_{n-4}

a_{142}
 a_{141}
 a_{140}
 a_{139}
 a_{138}

| | | | | | | |
|-----------|-----------|-----------|-----------|-----------|-----------|---|
| a_{146} | a_{147} | 0 | 0 | 0 | 0 | 0 |
| a_{145} | a_{146} | a_{147} | 0 | 0 | 0 | 0 |
| a_{144} | a_{145} | a_{146} | a_{147} | 0 | 0 | 0 |
| a_{143} | a_{144} | a_{145} | a_{146} | a_{147} | 0 | 0 |
| a_{142} | a_{143} | a_{144} | a_{145} | a_{146} | a_{147} | 0 |

Fig. 11

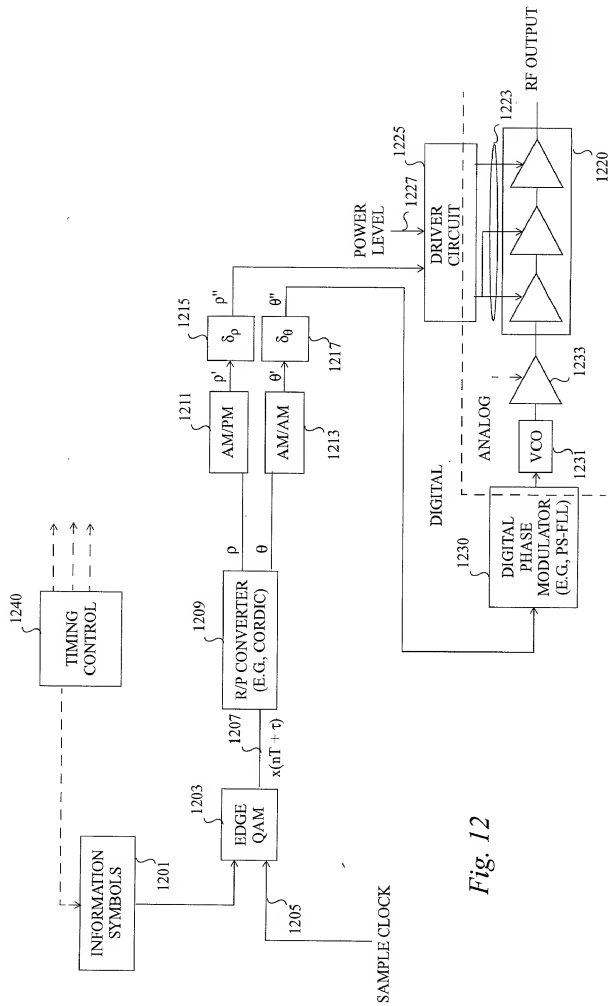


Fig. 12

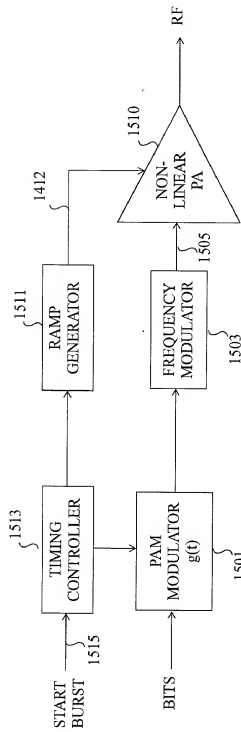


Fig. 15

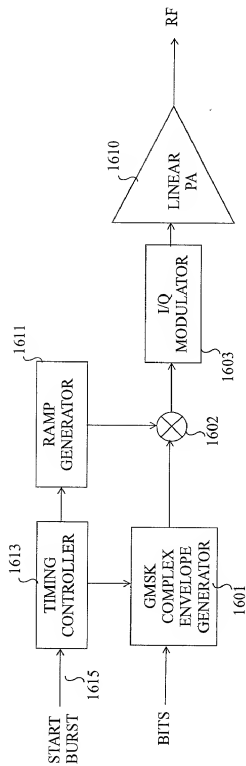


Fig. 16

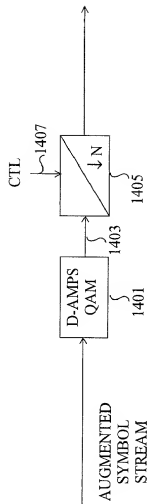


Fig. 14

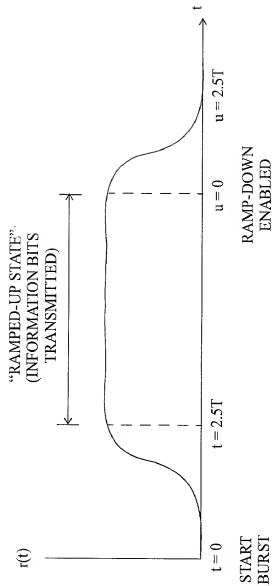


Fig. 17

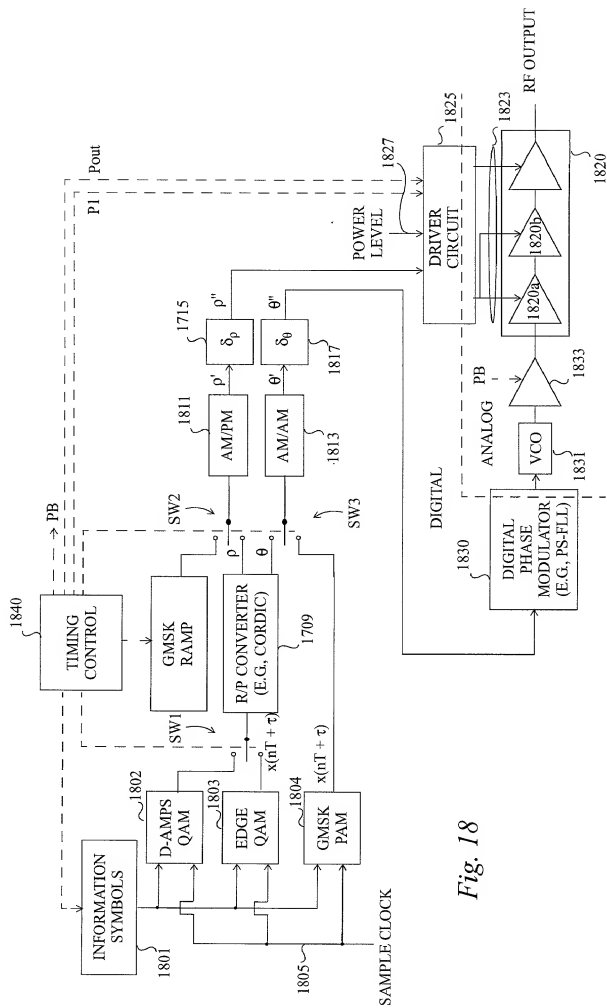


Fig. 18

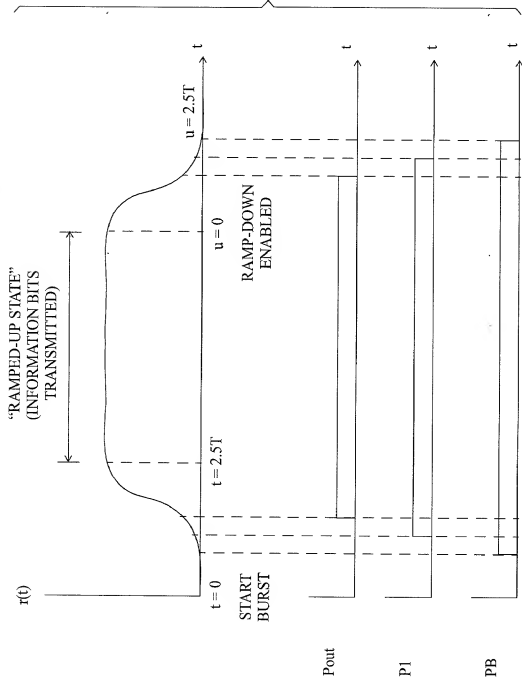


Fig. 19